SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION					ATTY. DOCKET NO. 57454-322			SERIAL NO.		
					APPLICANT Hideto HIDAKA					
(PTO-1449)					FILING DATE January 18, 2002			GROUP		
U.S. PATENT DOCUMENTS										
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME		CL	ASS	SUBCLASS	FII	LING DATE	
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EXAMINER'S INITIALS	PATENT NO.	DATE		COUNTRY	CLA	CLASS	SUBCLASS			
							-	Yes	No	
	OTHER	ART (Incl	l uding Autho	or. Title. Da	ıte. Pertinei	nt P	ages. Etc.)		<u> </u>	
18	Scheuerlein, et a Tunnel Junction	OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) Icheuerlein, et al., "A 10ns Read and Write Non-Volatile Memory Array Using A Magnetic Funnel Junction and FET Switch in Each Cell" ISSCC Digest of Technical Papers, TA7.2. Tebruary 2000, pages 94-95, 128-129,409-410								
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LP	US Patent Application Serial No. 09/887,321, Filed June 25, 2001, Atty. Docket No. 57454-137									
18	US Patent Application Serial No. 09/834,638, Filed April 16, 2001, Atty. Docket No. 57454-075									
IP	US Patent Application Serial No. 09/805,043, Filed March 14, 2001, Atty. Docket No. 57454-039									
LP	US Patent Application Serial No. 09/832,025, Filed April 11, 2001, Atty. Docket No. 57454-070									
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	EXAMINER DATE CONSIDERED									
12/2/2003										

EXAMPLER / Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.